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# United States Patent [19]

Sedlak

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[54] READ-ONLY MEMORY HAVING SPECIALLY OUTPUT CIRCUITS AND WORD LINE CONNECTED TO A GROUP OF MEMORY CELLS

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#### Related U.S. Application Data

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365/185.25

[58] Field of Search 365/230.03, 63,  
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185.18

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#### [57] ABSTRACT

A read-only memory having a multiplicity of memory cells whose contents can be read out with appropriate addressing by word, bit and source lines. The read-only memory is distinguished by the fact that the memory cells which can be addressed via an individual word line are divided into a multiplicity of groups, to each of which a separate common source line is assigned. Accordingly, a group-by-group read-out of the memory cells which can be addressed via an individual word line is carried out.

18 Claims, 3 Drawing Sheets

